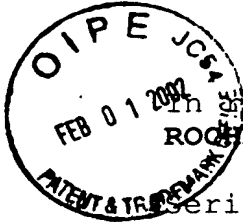


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In the Patent Application of:  
ROOYE ET AL.

Serial No. 09/995,251

Filing Date: NOVEMBER 27, 2001

For: MICROPROCESSOR COMPRISING  
INPUT MEANS IN THE TEST MODE

2858

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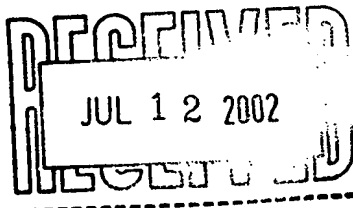
Director, U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

Enclosed are two (2) sheets of formal drawings to be  
filed in the above-identified patent application.

Respectfully submitted,

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